



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

LETTER TO OFFICIAL DRAFTSPERSON

Atty. Docket No. (Opt.)
UBAT1440Applicant
Stephen F. Smith, et al.Application Number
09/653,788Filed
September 1, 2000For:
**Digital-Data Receiver Synchronization Method
and Apparatus**Group Art Unit
2631Examiner
Unknown**RECEIVED**Confirmation No.
5601**AUG 21 2002**

Technology Center 2600

Certification Under 37 C.F.R. §1.8

I hereby certify that this document is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231 on August 13, 2002.

Janice Pampell
Janice Pampell

U.S. Patent and Trademark Office
Washington, D.C. 20231

Dear Sir:

Applicant hereby submits eight (8) pages of formal drawings and respectfully requests entry of these drawings.

REMARKS

Applicant has attached formal drawing pages to replace the informal drawing pages submitted with the initial application. Applicant respectfully submits that no new matter has been entered. Accordingly, Applicant respectfully requests that the drawings be approved.

While Applicant does not believe any fees are due and owing, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 50-0456 of Gray Cary Ware & Freidenrich LLP.

Respectfully submitted,

Gray Cary Ware & Freidenrich LLP
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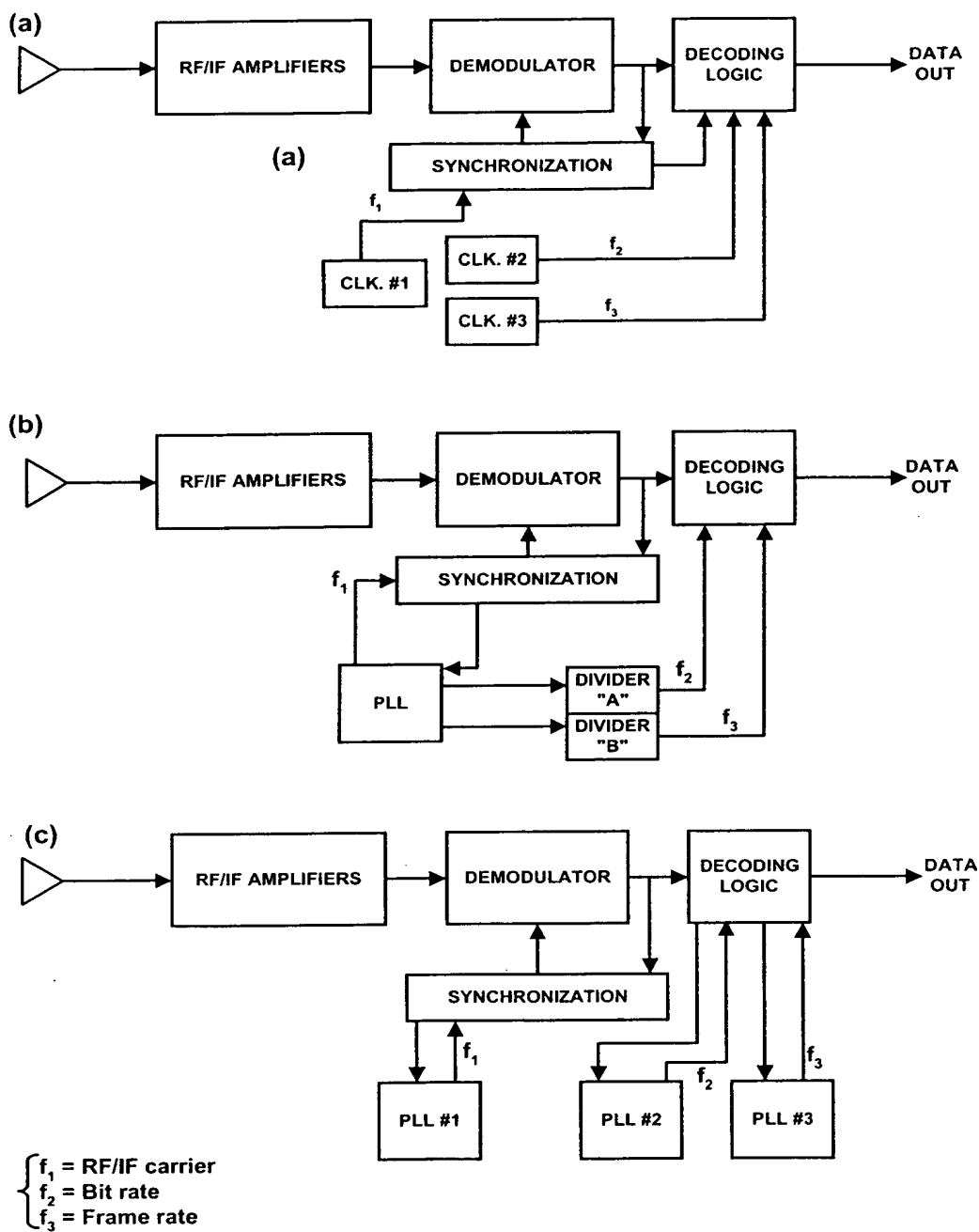


Fig. 1 [Prior Art]

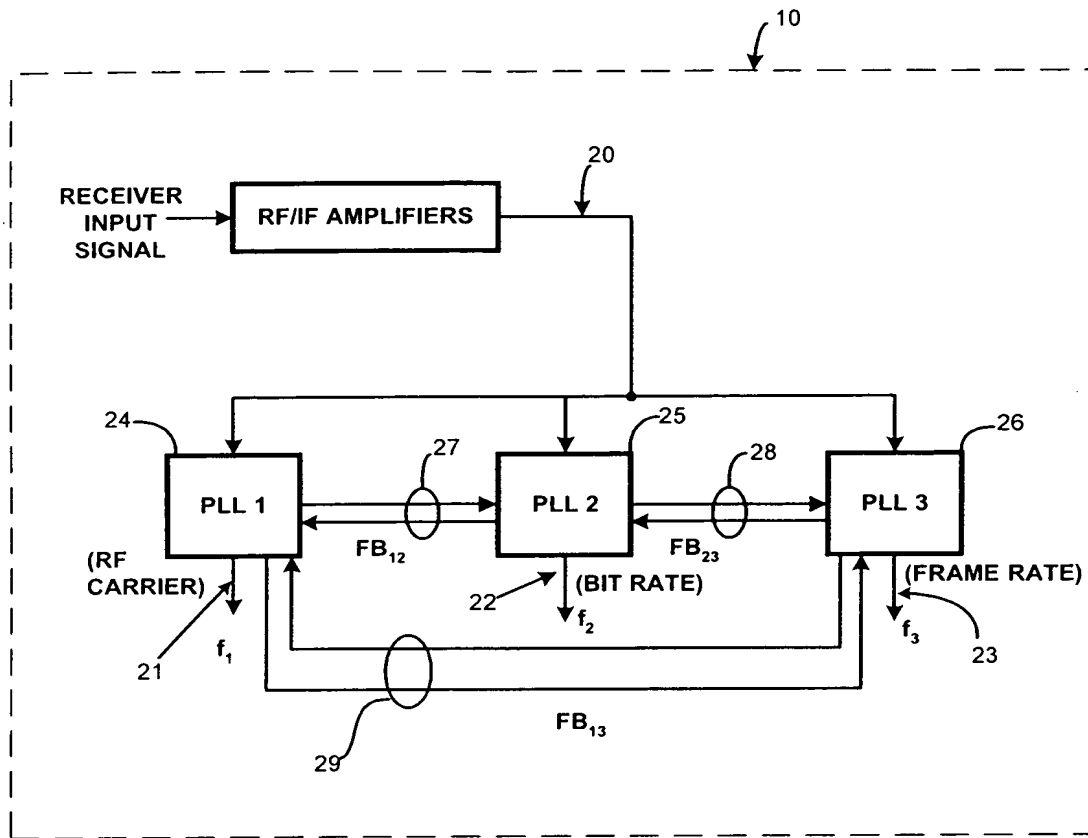


Fig. 2

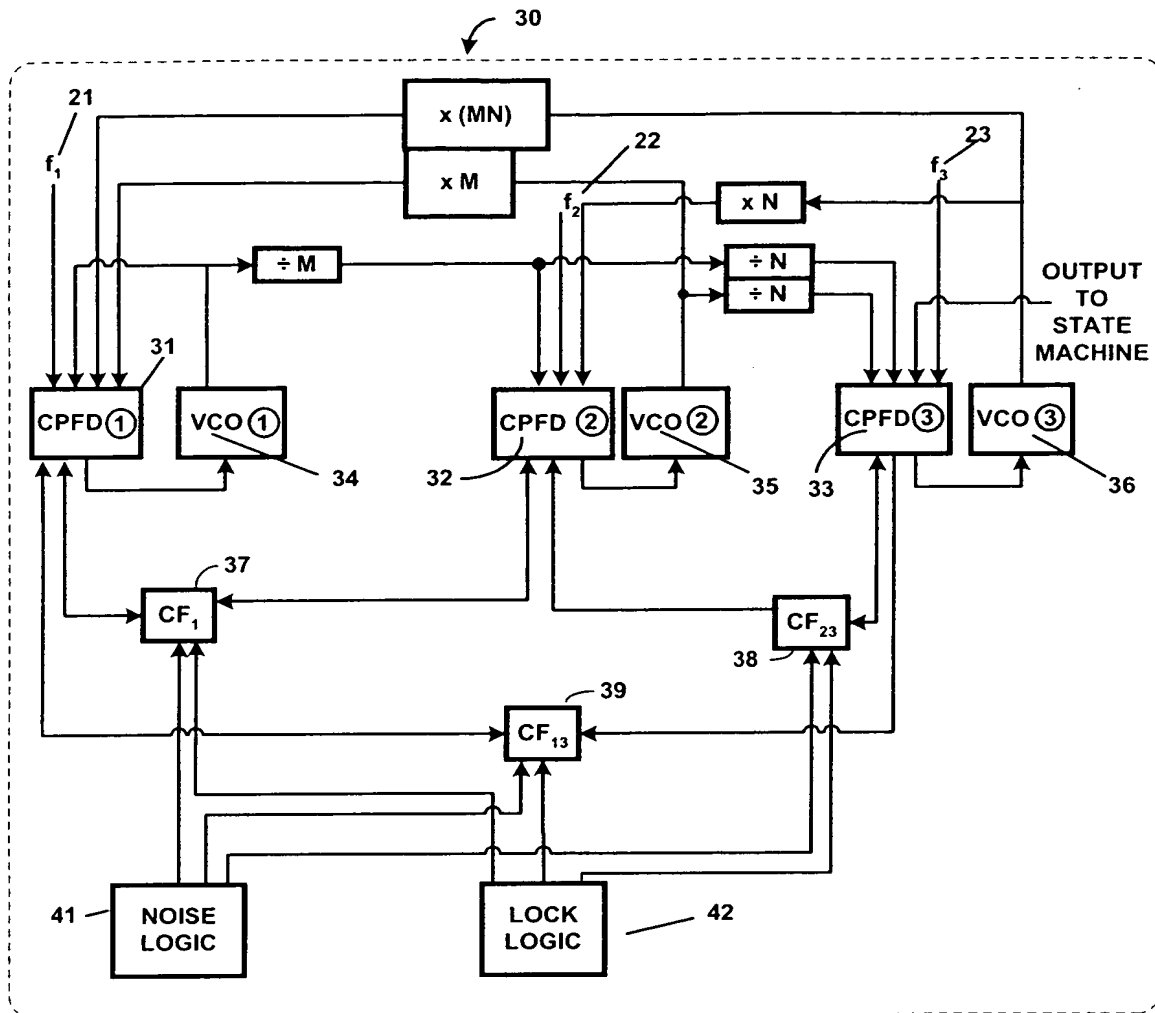
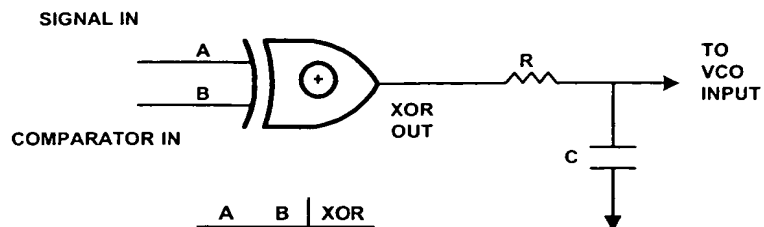
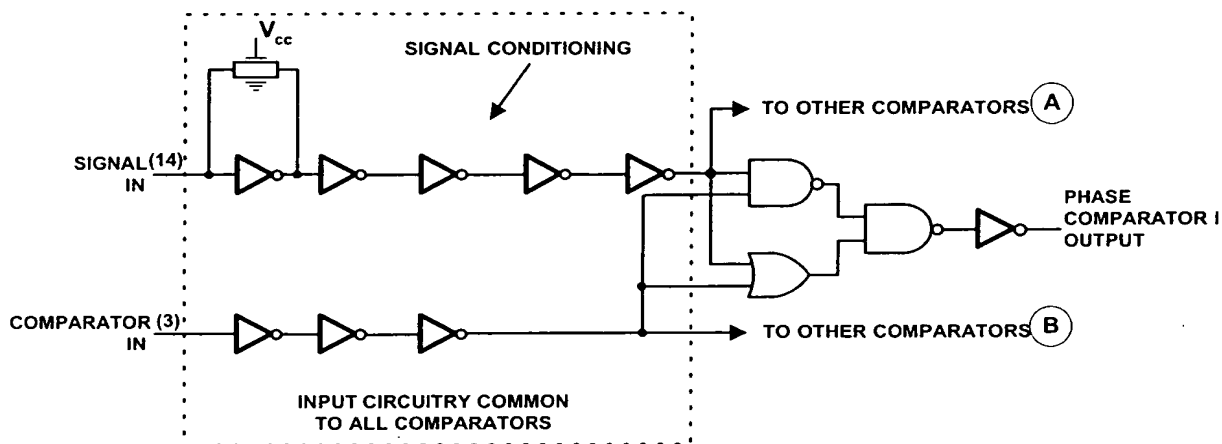


Fig. 3

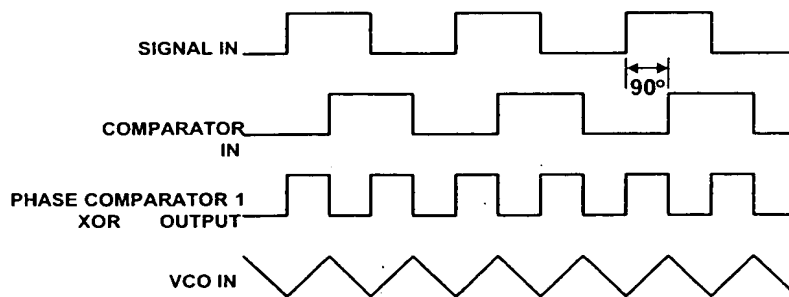


A	B	XOR
0	0	0
0	1	1
1	0	1
1	1	0

(A) BASIC XOR PHASE COMPARATOR

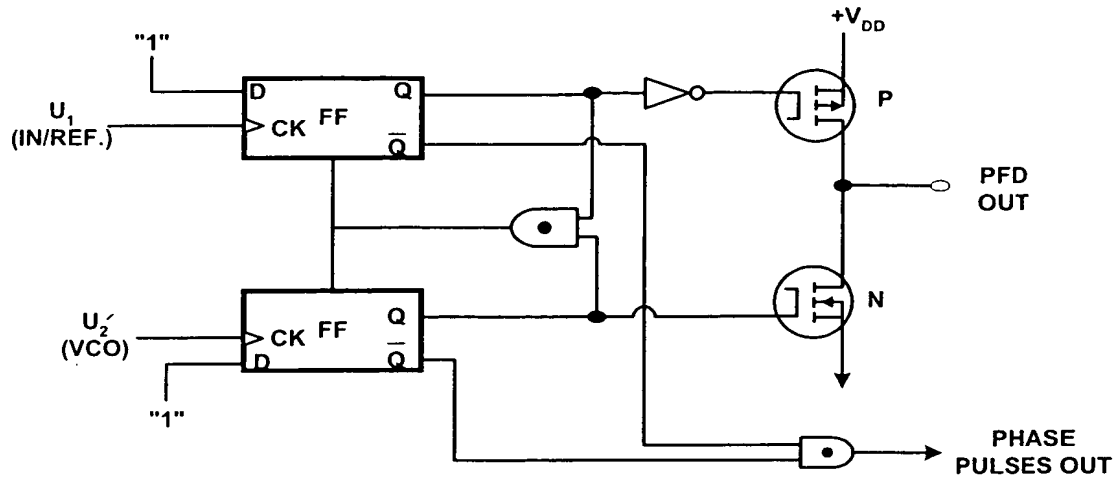


(B) DETAILED ON-CHIP IC CIRCUITRY IN 74HC4046

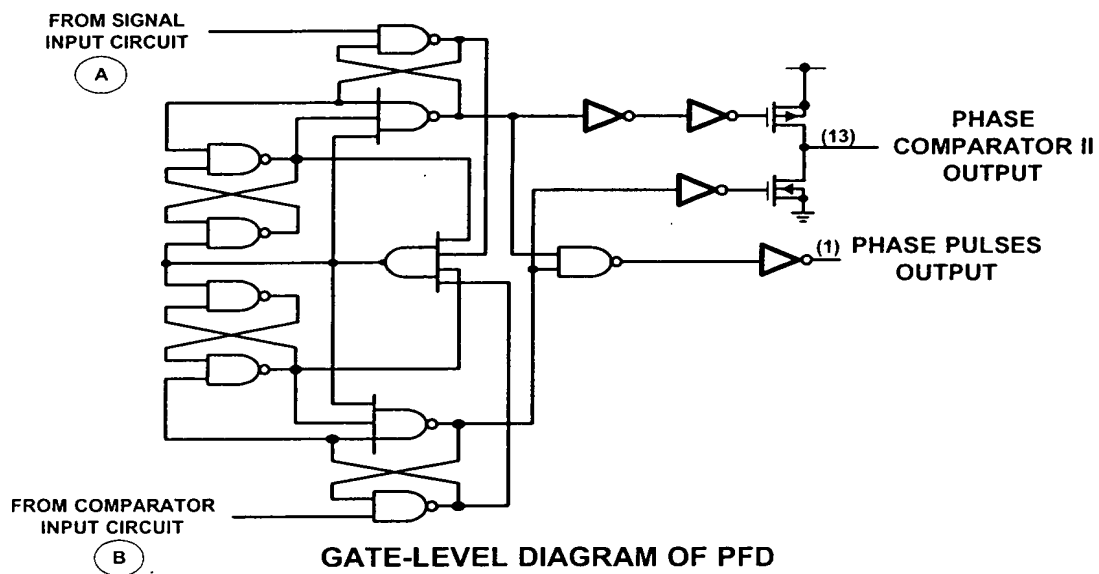


(C) TYPICAL XOR DETECTOR WAVEFORMS

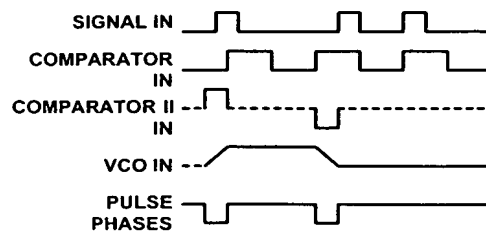
Fig. 4 (Prior Art)



HIGH-LEVEL DIAGRAM OF PFD (existing art)



GATE-LEVEL DIAGRAM OF PFD



TIMING AND WAVEFORMS OF PFD (existing art)

Fig. 4 cont. (Prior Art)

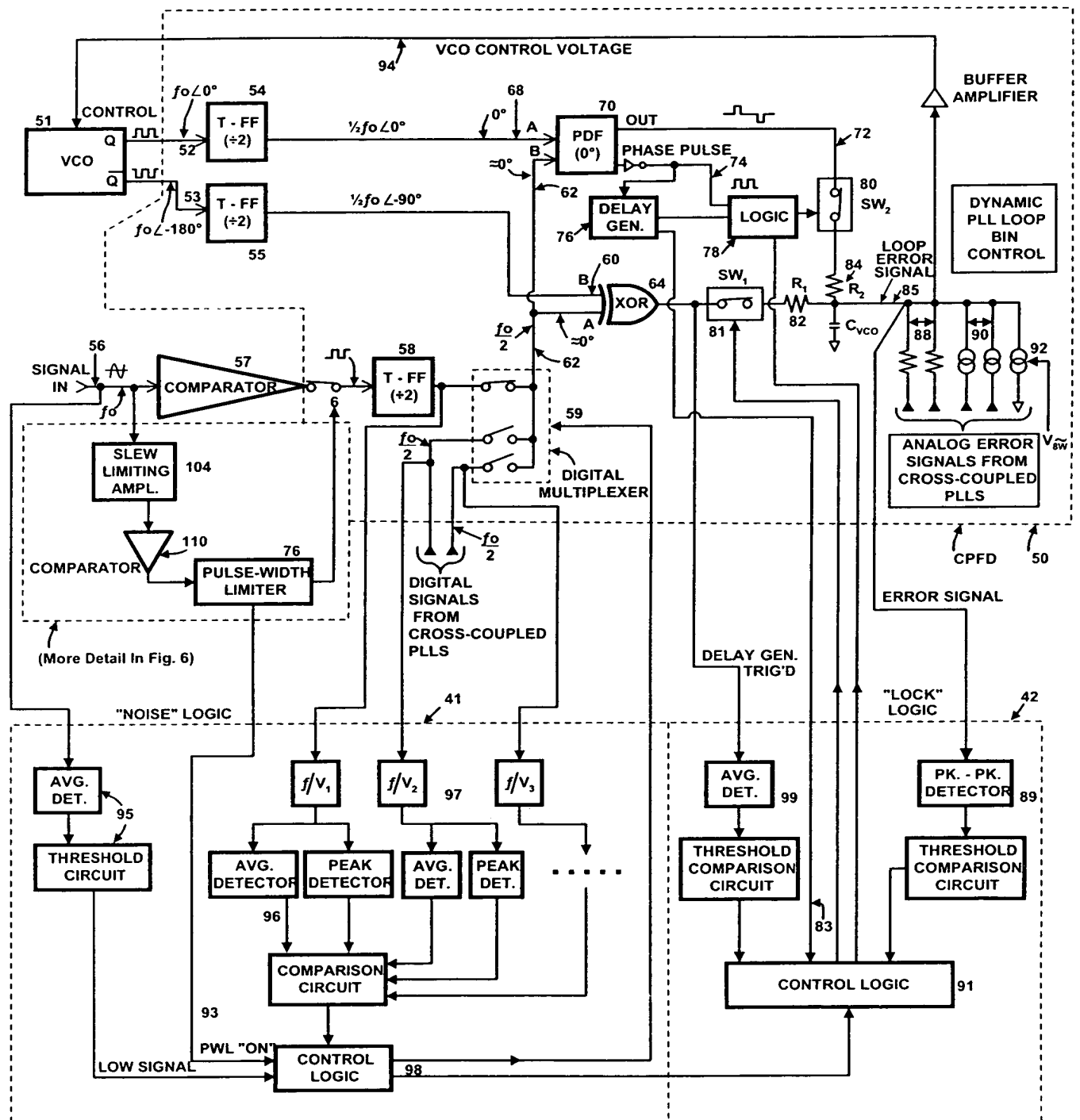


Fig. 5

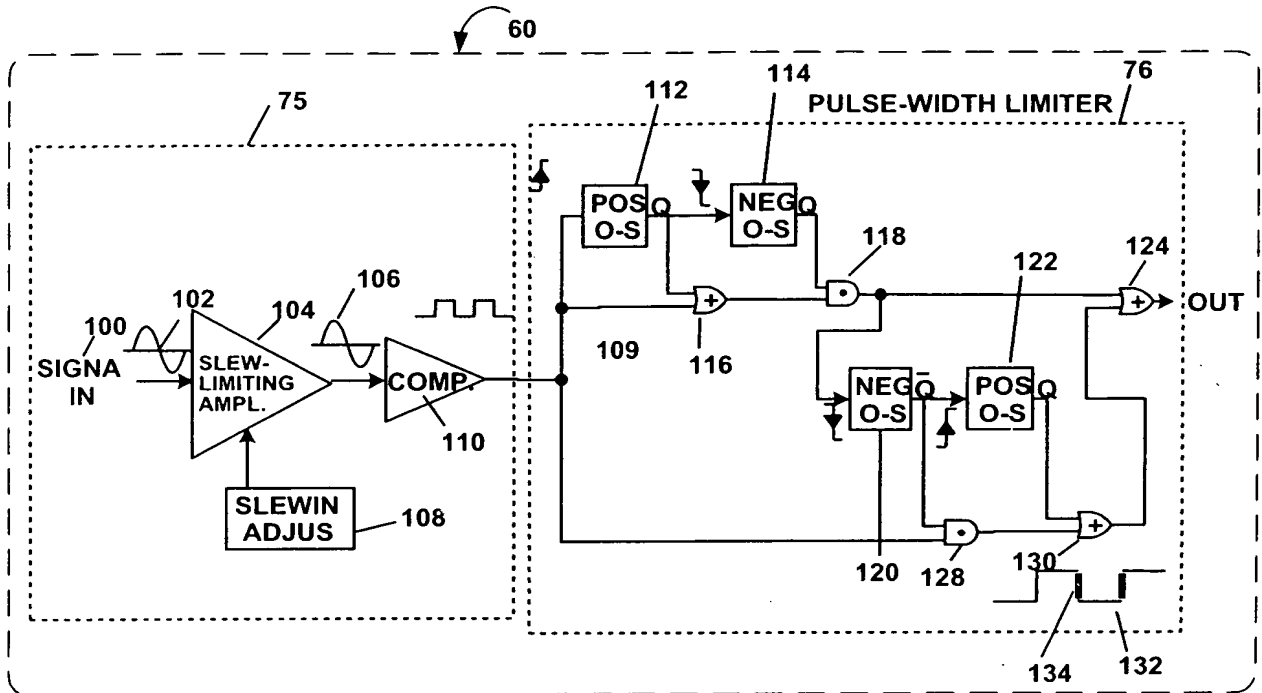


Fig. 6

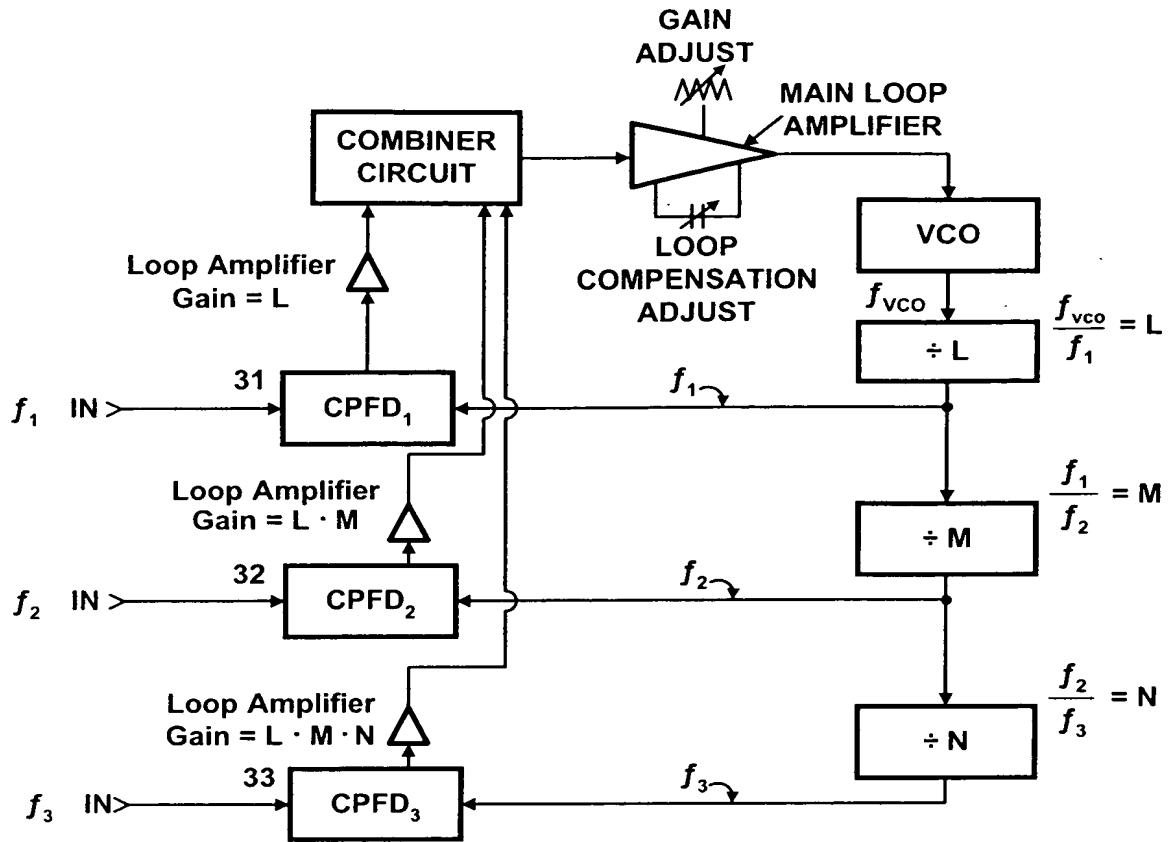


Fig. 7